

NAME: LAB SECTION:



RUTGERS

School of Engineering
Department of Electrical and Computer Engineering

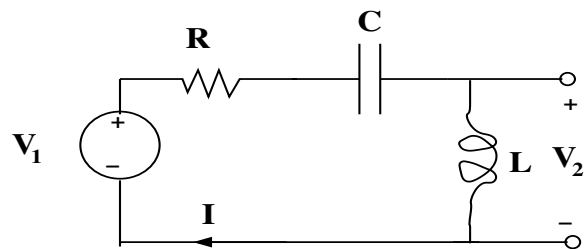
332:221

Principles of Electrical Engineering I
Quizlette 10

Fall 2012

*USING A CALCULATOR WILL SLOW YOU DOWN! Final answers must appear in the appropriate box.
Show your work outside the box.*

1. Basic Stuff:



Assume sinusoidal steady state operation at some frequency ω and suppose $V_1 = V e^{j\theta}$ and $I = I e^{j\phi}$.

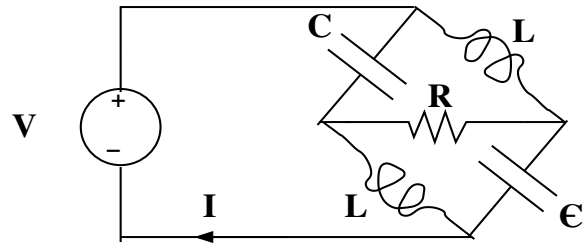
(a) (1 pts) What is the maximum value (amplitude) of the input voltage $V_1(t)$?

(b) (1 pts) What is the RMS value of the input voltage $V_1(t)$?

(c) (1 pts) Which is larger, $\max_t V_1(t)$ or $\{V_1(t)\}_{\text{RMS}}$

(d) (2 pts) What is the average power *supplied to* the resistor, capacitor and inductor?

2. **VERY Cute:** Assume sinusoidal steady state at some frequency ω .



- (a) (2 pt) Assume $\mathbf{V} = V e^{j\theta}$ and $\mathbf{I} = I e^{j\theta}$. What is the amplitude of the voltage across the resistor?

- (b) (3 pts) At what value of ω (if any) does V_R , the voltage across the resistor, equal zero?